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Flash which is organized on a byte or word basis, NAND Flash is organized into organization of the NAND Flash is conducive to lower cost per bit but not suitable shows 32 memory cells sandwiched between two large select transistors and two for random access NAND Flash is therefore employed as a data storage medium

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20 2 NAND overview: from memory to systems The most popular Flash memory cell is based on the Floating Gate (FG) technology, whose cross section is shown in Fig 21

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R Micheloni et al, Inside NAND Flash Memories (Springer, Netherlands, 2010) CrossRef Google Scholar 31 CH Hung, MF Chang et al, Layer-aware program and read schemes for 3D stackable Vertical-Gate BE-SONOS NAND flash against cross-layer process variations

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multi-core microcontroller plus NAND flash memories It provides a broad overview of the most popular simulation tools, with special focus on open source solutions Solid-State-Drives (SSDs) Modeling - Springer R Micheloni et al, Inside NAND Flash Memories (Springer, Netherlands, 2010) CrossRef Google Scholar 31 Page 3/8

Flash Memory Reliability— Read, Program, and Erase Latency ...

This report documents the efforts and results of the fiscal year (FY) 2010 NASA Electronic Parts and Packaging Program (NEPP) task for nonvolatile memory (NVM) reliability This year's focus was to measure latency (read, program, and erase) of NAND Flash memories and determine how these parameters drift with erase/program/read endurance cycling

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